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Master protocol chip - IBS IPMS 3 QFP - 2751807

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The INTERBUS protocol microcontroller IPMS-3 is a CMOS gate array. The IPMS-3 is used with a variety of processors



Key Commercial Data

Packing unit	66 STK
Minimum order quantity	66 STK
GTIN	 4 017918 272364
Sales Key	14

Technical data

Dimensions

Width	19.4 mm
Depth	25.4 mm

General

IC power supply voltage	5 V DC \pm 10 % (DC)
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Classifications

eCl@ss

eCl@ss 4.0	27250201
eCl@ss 4.1	27250201
eCl@ss 5.0	27250203
eCl@ss 5.1	27249201
eCl@ss 6.0	27242605
eCl@ss 7.0	27242605
eCl@ss 8.0	27242608

ETIM

ETIM 2.0	EC001433
ETIM 3.0	EC001601
ETIM 4.0	EC001601

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Classifications

ETIM

ETIM 5.0	EC001604
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UNSPSC

UNSPSC 6.01	43172015
UNSPSC 7.0901	43201404
UNSPSC 11	43172015
UNSPSC 12.01	43201404
UNSPSC 13.2	43201404

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